

CALL FOR PHD FORUM



30th INTERNATIONAL SYMPOSIUM ON VLSI DESIGN AND TEST (VDAT- 2026)

Jaypee Institute of Information Technology, Sector-62, Noida

Submission Info

<https://vdat.org.in/>

Format: The extended abstract must strictly adhere to the provided template and be in PDF format only. The font size should be a minimum of 10 pt, using the Times New Roman Font. The document should not exceed three pages, including tables, diagrams, and references.

Scan to submit



Presentation: 15 mins in-person at the conference venue.

Submit a single PDF containing all the information listed below:

1. **Title:** Title of the Ph.D. Forum, limited to 20 words.
2. **Abstract:** Clearly outlining the intent of the thesis.
3. **Keywords:** Provide at least 5 keywords.
4. **Research Contributions:** Please list at least 3 major research findings from the thesis.
5. **Brief Explanation of Research Outcomes:** A note on individual research contributions.

Research Dissemination: The research papers/tools published out of and strictly restricted to the thesis. Citing research papers unrelated to the thesis may lead to rejection of the Ph.D. forum report.

Domain of the Ph.D. Thesis: Aligned with any track in the conference. Please note that the thesis need not be exactly limited by the tracks listed.

Author Details: Qualification, Experience, etc. Please provide a biography of each speaker (limiting to 300 words each).

IMPORTANT DATES

Email: vdat26.jiit@gmail.com

30th June
2026

Submission
deadline

22th July
2026

Acceptance
notification

1st August
2026

Camera ready
submission

20-22th August
2026

Conference
date

Organizing Partners



Jaypee Institute of Information Technology, Noida



Delhi Technological University, Delhi



Indraprastha Institute of Information Technology, Delhi



Indira Gandhi Delhi Technical University for Women, New Delhi



Netaji Subhas University of Technology, Delhi



National Institute of Technology, Delhi